

REMARKS/ARGUMENTS

Favorable reconsideration of this application, as presently amended and in light of the following discussion, is respectfully requested.

Claims 1-36 are pending in this application, with Claims 1 and 19 having been amended. The changes and additions to the claims do not add new matter and are supported by the originally filed specification.

In the Notice of Allowance, Claims 1-36 were allowed.

Applicant notes that the Examiner did not indicate consideration of JP 2001-7706 listed in line AO of the Information Disclosure Statement (IDS) filed on January 6, 2004. A copy of JP 2001-7706 and an English translation was submitted to the USPTO on January 6, 2004 and is available in the image file wrapper for this application, as indicated in the USPTO's PAIR system. Accordingly, it is respectfully requested that JP 2001-7706, listed in the January 6, 2004 IDS, be indicated as considered.

Applicant submits herewith a certified copy of Applicant's Japanese priority document (JP Application No. 2002-300066). The Examiner appears to have inadvertently indicated that Applicant's certified copy of the priority document was received by the USPTO in the Notice of Allowance. Therefore, Applicant respectfully requests that the Examiner provide acknowledgement of Applicant's claim of foreign priority and that the certified copy of the priority document has been received in the next Office Action.

Applicant thanks the Examiner for the indication of allowable subject matter.

With respect to the amendment of Claims 1 and 19, Applicant respectfully submits that the amendment to Claims 1 and 19 are for removing language that does not change the allowability of Claims 1 and 19. Amended Claim 1 recites, *inter alia*,

arraying ~~all types of~~ predetermined proper signals
used for signals transmitted and bit strings of patterns
including errors each derived from each proper signal to
thereby generate a different two-dimensional image and
disposing a set of received signal patterns....

Amended Claim 19 recites features similar to those of amended Claim 1. Applicant respectfully submits that the Examiner's cited prior art reference, Boffi et al. (US Patent No. 6,275,311, hereafter "Boffi"), fails to disclose or suggest at least these features of amended Claims 1 and 19.

The Examiner's Reasons for Allowance state that the decoding method described in Boffi "would not involve 'arraying all types of predetermined proper signals used for signals transmitted and bit strings of patterns including errors each derived from each proper signal to thereby generate a different two-dimensional image and disposing a set of received signal patterns, each comprised of an arbitrary proper signal and a group of two-dimensional images of patterns having the arbitrary proper signal added with an error, in a state that enables identification of each proper signal to thereby generate a received signal pattern image' and use it to 'evaluate a coefficient of correlation between the received signal image and the received signal pattern image' *because the processing in Boffi et al. is preformed with logic gates and does not calculate a 'correlation projection image.'*" (See Notice of Allowance, at page 4). (Emphasis added).

Therefore, the Examiner's Reasons for Allowance do not state that Boffi's failure to disclose arraying "all types of" predetermined proper signals, as recited in Claim 1, is a specific or sole reason for allowance.

Thus, Applicant respectfully submits that amended Claims 1 and 19 (and all associated dependent claims) remain allowable.

Consequently, in light of the above discussion and in view of the present amendment, the present application is believed to be in condition for formal allowance. An early and favorable action to that effect is respectfully requested.

Respectfully submitted,

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